


## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

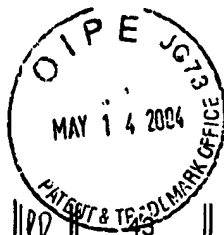
Electronic Version v18

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Title of Invention	Method and apparatus for selecting a route for a net based on the impact on other nets																																																									
Application Number:	10/066160																																																									
Confirmation Number:	3579																																																									
First Named Applicant:	Steven Teig																																																									
Attorney Docket Number:	SPLX.P0073																																																									
Art Unit:	2825																																																									
Examiner:	Paul Dinh																																																									
Search string:	( 4782193 or 5633479 or 5635736 or 6128767 or 6219823 or 6226560 or 6262487 or 6295634 or 6436804 or 6490713 or 6546540 or 6645842 or 6665852 or 4673966 or 4855929 or 5360948 or 5375069 or 5532934 or 5578840 or 5618744 or 5636125 or 5637920 or 5650653 or 5657242 or 5663891 or 5723908 or 5742086 or 5757656 or 5777360 or 5811863 or 5822214 or 5838583 or 5859449 or 5889329 or 5889677 or 5898597 or 5914887 or 5973376 or 5980093 or 6035108 or 6038383 or 6058254 or 6067409 or 6068662 or 6088519 or 6111756 or 6123736 or 6155725 or 6166441 or 6175950 or 20020104061 or 20020100009 or 20020107711 or 20020182844 or 20030005399 or 20030188281 or 20010003843 or 20020174413 or 20030025205 or 20030121017 ).pn.																																																									
<b>US Patent Documents</b>																																																										
Note: Applicant is not required to submit a paper copy of cited US Patent Documents																																																										
<table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>PP</td><td>1</td><td>4782193</td><td>1998-11-01</td><td>Linsker</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>5633479</td><td>1997-05-27</td><td>Hirano</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>5635736</td><td>1997-06-03</td><td>Funaki et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>4</td><td>6128767</td><td>2000-10-03</td><td>Chapman</td><td></td><td></td><td></td></tr><tr><td></td><td>5</td><td>6219823</td><td>2001-04-17</td><td>Hama et al.</td><td>B1</td><td></td><td></td></tr><tr><td>PO</td><td>6</td><td>6226560</td><td>2001-05-01</td><td>Hama et al.</td><td>B1</td><td></td><td></td></tr></tbody></table>	init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	PP	1	4782193	1998-11-01	Linsker					2	5633479	1997-05-27	Hirano					3	5635736	1997-06-03	Funaki et al.					4	6128767	2000-10-03	Chapman					5	6219823	2001-04-17	Hama et al.	B1			PO	6	6226560	2001-05-01	Hama et al.	B1				
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8	6262487	2001-07-17	Igarashi et al.	B1
9	6295634	2001-09-25	Matsumoto	B1
10	6436804	2002-08-20	Igarashi et al.	B2
11	6490713	2002-12-03	Matsumoto	B2
12	6546540	2003-04-08	Igarashi et al.	B1
13	6645842	2003-11-11	Igarashi et al.	B2
14	6665852	2003-12-16	Xing et al.	B2
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19	5532934	1996-07-02	Rostoker	
20	5578840	1996-11-26	Scepanovic et al.	
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	47	6123736	2000-09-26	Pavisic et al.	
	48	6155725	2000-12-05	Scepanovic et al.	
	49	6166441	2000-12-26	Geryk	
PD	50	6175950	2001-01-16	Scepanovic et al.	B1

### US Published Applications

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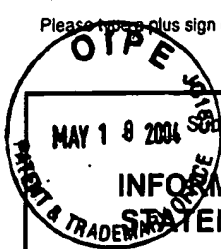
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
PD	1	20020104061	2002-08-01	Xing et al.	A1		
	2	20020100009	2002-07-25	Xing et al.	A1		
	3	20020107711	2002-08-08	Xing et al.	A1		
	4	20020182844	2002-12-05	Igarashi et al.	A1		
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	6	20030188281	2003-10-02	Xing	A1		
	7	20010003843	2001-06-14	Scepanovic et al.	A1		
	8	20020174413	2002-11-21	Tanaka	A1		
	9	20030025205	2003-02-06	Shively	A1		
PD	10	20030121017	2003-06-26	Andreev et al.	A1		

### Signature

Examiner Name	Date
PAUL DINH	7/7/04

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Substitute for form 1449A/PTO				Application Number	10/066,160
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Filing Date	1/31/2002
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	Dinh, Paul
Sheet	1	of	9	Attorney Docket Number	SPLX.P0073

U.S. PATENT APPLICATIONS						
Examiner* Initials	Cite No. <sup>1</sup>	U.S. Patent Application		Name of Patentee or Applicant of Cited Document	Date of Filing MM-DD-YYYY	Related Application Data if any
		Serial Number	Attorney Docket Number			
PD	1.	10/066,060	SPLX.P0072	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	2.	10/066,095	SPLX.P0074	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	3.	10/066,047	SPLX.P0078	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	4.	10/061,641	SPLX.P0079	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	5.	10/066,094	SPLX.P0080	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	6.	10/076,121	SPLX.P0081	Steven Teig et al.	02-12-2002	CIP of 10/066,094.
	7.	10/062,995	SPLX.P0105	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	8.	10/066,102	SPLX.P0106	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	9.	10/066,187	SPLX.P0133	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	10.	10/286,584	CDN.P0037	Steven Teig	10-31-2002	
	11.	10/335,087	CDN.P0038	Steven Teig et al.	12-31-2002	
PD	12.	10/335,239	CDN.P0039	Steven Teig et al.	12-31-2002	

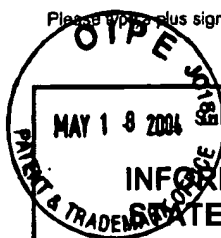
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/066,160
				Filing Date	1/31/2002
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	Dinh, Paul
Sheet	2	of	9	Attorney Docket Number	SPLX.P0073
<b>U.S. PATENT APPLICATIONS</b>					
PD	13.	10/335,086	CDN.P0040	Steven Teig et al.	12-31-2002

FOREIGN PATENT DOCUMENTS							
Examiner's Initials	Cite No. <sup>1</sup>	Foreign Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup> (if known) <sup>5</sup>				
PD	14.	JP	11-296560.	10-29-1999	Matsumoto et al.	with English translation of Abstract;	
PD	15.	JP	2000-082743	03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	16.	JP	64-15947	01-19-1989	Ouchi	with English translation of Abstract;	
	17.	JP	03-173471	07-26-1991	Tawada et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	18.	JP	04-000677	01-06-1992	Fujiwara et al.	with English translation of Abstract;	
	19.	JP	05-102305	04-23-1993	Sato	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
PD	20.	JP	05-243379	09-21-1993	Kubota	with Japanese Patent Office's English translation of Abstract;	✓

Examiner Signature	Paul Dinh	Date Considered	7/6/04
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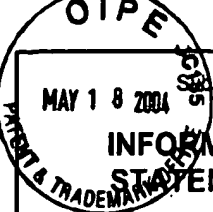
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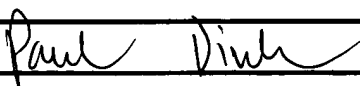
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 Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/066,160		
				Filing Date	1/31/2002		
				First Named Inventor	Steven Teig, et al.		
				Group Art Unit	2825		
				Examiner Name	Dinh, Paul		
Sheet	3	of	9	Attorney Docket Number	SPLX.P0073		
<b>FOREIGN PATENT DOCUMENTS</b>							
1						and with English translation of the application.	
PD	21.	JP	07-086407	03-31-1995	Miura	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
PD	22.	JP	09-162279	06-20-1997	Yoshida	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓

<b>NON PATENT LITERATURE DOCUMENTS</b>			
Examiner's Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
PD	23.	Chen, H.F. et al., A Faster Algorithm for Rubber-Band Equivalent Transformation for Planar VLSI Layouts, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 15, No. 2, February 1996, pp. 217-227.	
	24.	Chip Model with Wiring Cost Map, August 1983, IBM Technical Disclosure Bulletin, vol. 26, issu. 3A, pp. 929-933	
	25.	Dayan, T. et al., Layer Assignment for Rubber Band Routing, UCSC-CRI-93-04, January 20, 1993.	
	26.	Dayan, T., Rubber-Band Based Topological Router, A Dissertation, UC Santa Cruz, June 1997.	
PD	27.	Dood, P. et al. A Two-Dimensional Topological Compactor with Octagonal Geometry, 28 <sup>th</sup> ACM/IEEE Design Automation Conference, pp 727-731, July 1991.	

Examiner Signature		Date Considered	7/6/04
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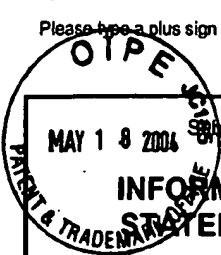
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				<b>First Named Inventor</b> Steven T ig, et al.
				<b>Group Art Unit</b> 2825
				<b>Examiner Name</b> Dinh, Paul
<b>Sheet</b> 4	<b>of</b> 9	<b>Attorney Docket Number</b> SPLX.P0073		

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PD	28.	Fujimura, K. et al, Homotopic Shape Deformation.	
	29.	Hama, T. et al., Curvilinear Detailed Routing Algorithm and its Extension to Wire-Spreading and Wire-Fattening.	
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	39.	Su, J. et al., Post-Route Optimization for Improved Yield Using Rubber-Band Wiring Model, 1997 International Conference on Computer-Aided Design, pp 700-706, November 1997.	
	40.	Wei-Ming Dai, W. et al., Routability of a Rubber-Band Sketch. 28 <sup>th</sup> ACM-IEEE Design Automation Conference, 1991. pp. 45-65.	
	41.	Xing, Z. et al., A Minimum Cost Path Search Algorithm Through Tile Obstacles, slide presentation.	
PD	42.	Xing, Z. et al., Shortest Path Search Using Tiles and Piecewise Linear Cost Propagation, IEEE, 2002, pp.145-158.	

<b>Examiner Signature</b> Paul Dinh	<b>Date Considered</b> 7/6/04
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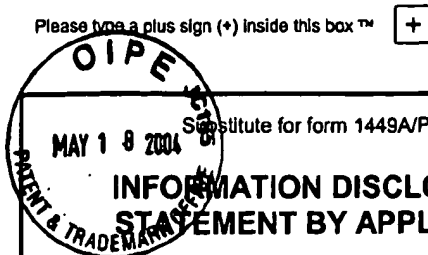
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Sheet				5	of	9	Application Number	10/066,160
							Filing Date	1/31/2002
							First Named Inventor	Steven Teig, et al.
							Group Art Unit	2825
							Examiner Name	Dinh, Paul
							Attorney Docket Number	SPLX.P0073

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Examiner Signature	<i>Paul Dinh</i>	Date Considered	7/6/04
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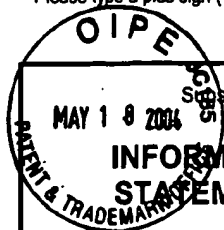
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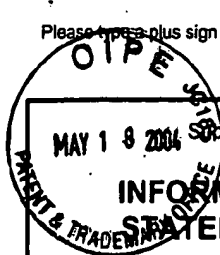
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				Filing Date	1/31/2002
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	Dinh, Paul
Sheet	7	of	9	Attorney Docket Number	SPLX.P0073

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				Group Art Unit	2825
				Examiner Name	Dinh, Paul
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